

**Table 3-1. Absolute Maximum Ratings<sup>0</sup>**

Parameter	Symbol	Comments	Value	Note: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.
Positive supply voltage	$V_{CC}$		GND to 4.6	
Temperature stabilization power	TPP		GND to 4.6	
Front plane	FPL		GND to $V_{CC} + 0.5$	
Digital input voltage	RST PCLK		GND to $V_{CC} + 0.5$	
Storage temperature	$T_{stg}$		-50 to +95	
Lead temperature (soldering, 10 seconds)	$T_{leads}$	Do not solder	<b>Forbidden</b>	

**Table 3-2. Recommended Conditions Of Use**

Parameter	Symbol	Comments	Min	Typ	Max	Unit
Positive supply voltage	$V_{CC}$		3V	3.3V	3.6V	V
Front plane	FPL	Must be grounded	GND			V
Digital input voltage			CMOS levels			V
Digital output voltage			CMOS levels			V
Digital load	$C_L$				50	pF
Analog load	$C_A$ $R_A$	Not connected				pF k $\Omega$
Operating temperature range	$T_{amb}$	V grade	-40°C to +85°C			°C
Maximum current on TPP	ITPP		0		100	mA